

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	35621	transformer WITH winding SAME (terminal outlet end solder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 10:35
L6	1497	4 and (pcb or (printed ADJ circuit ADJ board))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 11:25
L7	1944530	bobbin OR frame OR (coil ADJ1 former)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 11:26
L8	510	7 AND 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 11:56
L9	222	(EE OR EI) ADJ1 core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 11:55
L10	82	EE ADJ1 core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 11:40
L11	46	IE NEAR core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 11:40
L12	116	EE NEAR core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 11:40
L13	94	7 AND 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 11:41
L14	3506	(E or I) ADJ1 core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 11:55
L15	30	8 AND 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 12:03
L16	4	8 AND 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/22 12:03